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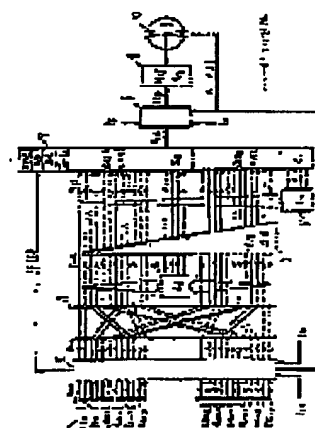
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### (54) DIGITAL SIGNAL RECORDING AND REPRODUCING DEVICE

#### (57)Abstract

**PURPOSE:** To eliminate the need for the change of an analog circuit and to process a signal in a same circuit system by adding a 1st time base conversion means, a format control means, a 2nd time base conversion means and a modulation/recording amplifier means to the recording side and providing each function means executing the inverse processing of each function for the recording side at a reproducing side.

**CONSTITUTION:** The 1st time base conversion means 2, a 1st error correction code generating means 4, an incomplete type ununiform delay interleave memory means 5, a 2nd error correction code generating means 6, a format control means 7, a 2nd time base conversion means 8 and the modulation/recording amplification means 9 are provided at the recording side. Further, each function means executing the inverse processing of each function of the recording side is provided at the reproduction side. Thus, the processing delay at recording/ reproduction is selected nearly to be a maximum delay at interleaving and the 1st time base conversion means 2 is provided so that the revision of the recording/reproduction analog circuit is not required for plural different sampling frequencies and the circuit system is used in common.



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